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Page: 4/17

Date: 2005/11/21 上午 09:32:52

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SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR CIRCUIT

Appl. No.

10/709,461

Confirmation No. 3460

Applicant

Chi-Ming Hsiao,

Guang-Kaai Dehng,

Ming-Horng Tsai,

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Filed

May 6, 2004

TC/A.U.

2816

Examiner

LAM, TUAN THIEU

Docket No.

MTKP0067USA1

Customer No.

27765

Commissioner for Patents

P.O. Box 1450

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Subject:

Response to the Office Action mailed 09/20/2005

INTRODUCTORY COMMENTS

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks, these detailing and providing supporting for the amendments herein, begin on page 13 of this paper.

1